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 PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = BIRACREE

First Name = STEPHEN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09761303</a>	Not Issued	71	01/17/2001	Blind cost criterion timing recovery	BIRACREE, STEPHEN L.
<a href="#">09922006</a>	Not Issued	93	08/03/2001	CARRIER PHASE ESTIMATION BASED ON SINGLE-AXIS CONSTANT MODULUS COST CRITERION AND BUSSGANG CRITERIA	BIRACREE, STEPHEN L.
<a href="#">10016665</a>	Not Issued	71	12/11/2001	Linear prediction based initialization of a single-axis blind equalizer for VSB signals	BIRACREE, STEPHEN L.
<a href="#">60242306</a>	Not Issued	159	10/20/2000	Constant modulus timing recovery	BIRACREE, STEPHEN L.
<a href="#">60254829</a>	Not Issued	159	12/12/2000	Linear prediction based initialization of single-axis blind equalizers for vsb modulations	BIRACREE, STEPHEN L.
<a href="#">09457496</a>	<a href="#">6668014</a>	150	12/09/1999	EQUALIZER METHOD AND APPARATUS USING CONSTANT MODULUS ALGORITHM BLIND EQUALIZATION AND PARTIAL DECODING	BIRACREE, STEPHEN L.

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**Inventor Name Search Result**

Your Search was:

Last Name = TOUZNI

First Name = AZZEDINE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09761303</u>	Not Issued	71	01/17/2001	Blind cost criterion timing recovery	TOUZNI, AZZEDINE
<u>09922006</u>	Not Issued	93	08/03/2001	CARRIER PHASE ESTIMATION BASED ON SINGLE-AXIS CONSTANT MODULUS COST CRITERION AND BUSSGANG CRITERIA	TOUZNI, AZZEDINE
<u>10016665</u>	Not Issued	71	12/11/2001	Linear prediction based initialization of a single-axis blind equalizer for VSB signals	TOUZNI, AZZEDINE
<u>10213854</u>	Not Issued	30	08/07/2002	DC offset correction for constant modulus equalization	TOUZNI, AZZEDINE
<u>10663028</u>	Not Issued	30	09/15/2003	Method and apparatus for controlling a smart antenna using metrics derived from a single carrier digital signal	TOUZNI, AZZEDINE
<u>10757536</u>	Not Issued	30	01/15/2004	Digital receiver having adaptive carrier recovery circuit	TOUZNI, AZZEDINE
<u>10973486</u>	Not Issued	30	10/26/2004	Trellis decoder for decoding data stream including symbols coded with multiple convolutional codes	TOUZNI, AZZEDINE
<u>11124131</u>	Not Issued	30	05/09/2005	Systems, methods, and apparatus for phase noise mitigation	TOUZNI, AZZEDINE
<u>60242306</u>	Not Issued	159	10/20/2000	Constant modulus timing recovery	TOUZNI, AZZEDINE
<u>60248941</u>	Not Issued	159	11/15/2000	Carrier phase detector based on single-axis constant modulus criterion	TOUZNI, AZZEDINE
<u>60254829</u>	Not Issued	159	12/12/2000	Linear prediction based initialization of single-axis blind equalizers for vsb modulations	TOUZNI, AZZEDINE
<u>60313161</u>	Not	159	08/17/2001	Behavior and corrections of	TOUZNI, AZZEDINE

	Issued			constant modulus equalization with a DC offset	
<a href="#">60475736</a>	Not Issued	159	06/04/2003	Method and apparatus for controlling a smart antenna using metrics derived from a single carrier digital signal	TOUZNI, AZZEDINE
<a href="#">60629433</a>	Not Issued	20	11/19/2004	Iterative decoding of packet data	TOUZNI, AZZEDINE
<a href="#">60707535</a>	Not Issued	20	08/12/2005	Systems, methods, and apparatus for impulse noise mitigation	TOUZNI, AZZEDINE

Inventor Search Completed: No Records to Display.

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PALM INTRANET

**Inventor Name Search Result**

Your Search was:

Last Name = ENDRES

First Name = THOMAS

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">06583344</a>	<a href="#">4578846</a>	150	02/24/1984	APPARATUS FOR RIVET ATTACHMENT	ENDRES, THOMAS E.
<a href="#">07562033</a>	Not Issued	161	08/02/1990	VERTICAL MEASUREMENT SYSTEM	ENDRES, THOMAS E.
<a href="#">07785085</a>	<a href="#">5169047</a>	150	10/30/1991	COMPACT RIVET ATTACHMENT APPARATUS	ENDRES, THOMAS E.
<a href="#">07994764</a>	<a href="#">5307698</a>	150	12/22/1992	VERTICAL MEASUREMENT SYSTEM	ENDRES, THOMAS E.
<a href="#">09549368</a>	Not Issued	93	04/14/2000	HYBRID SOFT AND HARD DECISION FEEDBACK EQUALIZER	ENDRES, THOMAS J.
<a href="#">09549369</a>	Not Issued	161	04/14/2000	Synchronization method and apparatus for a convolutional decoder	ENDRES, THOMAS J.
<a href="#">09638044</a>	Not Issued	161	08/12/2000	Constant modulus decision feedback equalizer	ENDRES, THOMAS J.
<a href="#">09716651</a>	<a href="#">6850563</a>	150	11/20/2000	DATA SLICER FOR COMBINED TRELLIS DECODING AND EQUALIZATION	ENDRES, THOMAS J.
<a href="#">09761303</a>	Not Issued	71	01/17/2001	Blind cost criterion timing recovery	ENDRES, THOMAS J.
<a href="#">09922006</a>	Not Issued	93	08/03/2001	CARRIER PHASE ESTIMATION BASED ON SINGLE-AXIS CONSTANT MODULUS COST CRITERION AND BUSSGANG CRITERIA	ENDRES, THOMAS J.
<a href="#">10016665</a>	Not Issued	71	12/11/2001	Linear prediction based initialization of a single-axis blind equalizer for VSB signals	ENDRES, THOMAS J.
<a href="#">10213854</a>	Not Issued	30	08/07/2002	DC offset correction for constant modulus equalization	ENDRES, THOMAS J.

<u>10246084</u>	Not Issued	30	09/18/2002	Adaptive expanded information capacity for communications systems	ENDRES, THOMAS J.
<u>10322299</u>	Not Issued	30	12/17/2002	Self-initializing decision feedback equalizer with automatic gain control	ENDRES, THOMAS J.
<u>10327280</u>	Not Issued	30	12/20/2002	Joint adaptive optimization of soft decision device and feedback equalizer	ENDRES, THOMAS J.
<u>10384048</u>	Not Issued	95	03/07/2003	DIVERSITY RECEIVER WITH JOINT AUTOMATIC GAIN CONTROL SIGNAL PROCESSING	ENDRES, THOMAS J.
<u>10408881</u>	<u>6937677</u>	150	04/09/2003	DIVERSITY RECEIVER WITH JOINT PHASE LOCKED LOOP FILTER	ENDRES, THOMAS J.
<u>10409371</u>	<u>6870892</u>	150	04/09/2003	DIVERSITY RECEIVER WITH JOINT BAUD CLOCK RECOVERY	ENDRES, THOMAS J.
<u>10782316</u>	Not Issued	30	02/19/2004	Joint, adaptive control of equalization, synchronization, and gain in a digital communications receiver	ENDRES, THOMAS J.
<u>10832102</u>	Not Issued	30	04/26/2004	Decision feedback equalization with fractionally-spaced feedback data	ENDRES, THOMAS J.
<u>10980879</u>	Not Issued	30	11/02/2004	Remote antenna, such as for indoor reception of broadcast television signals	ENDRES, THOMAS J.
<u>11109454</u>	Not Issued	19	04/18/2005	Remote antenna and local receiver subsystems for receiving data signals carried over analog television	ENDRES, THOMAS J.
<u>11109464</u>	Not Issued	20	04/18/2005	Symbol error based compensation methods for nonlinear amplifier distortion	ENDRES, THOMAS J.
<u>60215703</u>	Not Issued	159	07/01/2000	Robust data extension for 8VSB signaling	ENDRES, THOMAS J.
<u>60242306</u>	Not Issued	159	10/20/2000	Constant modulus timing recovery	ENDRES, THOMAS J.
<u>60248941</u>	Not Issued	159	11/15/2000	Carrier phase detector based on single-axis constant modulus criterion	ENDRES, THOMAS J.
<u>60254829</u>	Not Issued	159	12/12/2000	Linear prediction based initialization of single-axis blind	ENDRES, THOMAS J.

				equalizers for vsb modulations	
<u>60313161</u>	Not Issued	159	08/17/2001	Behavior and corrections of constant modulus equalization with a DC offset	ENDRES, THOMAS J.
<u>60341931</u>	Not Issued	159	12/18/2001	Self-initializing decision feedback equalizer with automatic gain control	ENDRES, THOMAS J.
<u>60448643</u>	Not Issued	159	02/19/2003	Joint, adaptive control of equalization, synchronization, and gain in a digital communications receiver	ENDRES, THOMAS J.
<u>60465819</u>	Not Issued	159	04/25/2003	Decision feedback equalization with fractionally-spaced feedback data	ENDRES, THOMAS J.
<u>60517159</u>	Not Issued	159	11/03/2003	Antenna system	ENDRES, THOMAS J.
<u>60529397</u>	Not Issued	159	12/12/2003	Remote antenna, such as for indoor reception of broadcast television signals	ENDRES, THOMAS J.
<u>60562713</u>	Not Issued	159	04/16/2004	Data insertion techniques for expanding information capacity of existing communication systems	ENDRES, THOMAS J.
<u>60562716</u>	Not Issued	159	04/16/2004	Symbol error based compensation methods for nonlinear amplifier distortion	ENDRES, THOMAS J.
<u>60562814</u>	Not Issued	159	04/16/2004	Remote antenna and local receiver subsystems for receiving data signals carried over analog television	ENDRES, THOMAS J.
<u>09099730</u>	<u>6178209</u>	150	06/19/1998	A METHOD OF ESTIMATING TRELLIS ENCODED SYMBOLS UTILIZING SIMPLIFIED TRELLIS DECODING	ENDRES, THOMAS J.
<u>09100705</u>	<u>6426972</u>	150	06/19/1998	REDUCED COMPLEXITY EQUALIZER FOR MULTI MODE SIGNALING	ENDRES, THOMAS J.
<u>09231888</u>	<u>6418164</u>	150	01/14/1999	ADAPTIVE EQUALIZER WITH ENHANCED ERROR QUANTIZATION	ENDRES, THOMAS J.
<u>09261843</u>	<u>6337878</u>	150	03/03/1999	ADAPTIVE EQUALIZER WITH DECISION DIRECTED CONSTANT MODULUS ALGORITHM	ENDRES, THOMAS J.
<u>09363813</u>	<u>6560299</u>	150	07/30/1999	DIVERSITY RECEIVER WITH JOINT SIGNAL PROCESSING	ENDRES, THOMAS J.

<a href="#">09457496</a>	<a href="#">6668014</a>	150	12/09/1999	EQUALIZER METHOD AND APPARATUS USING CONSTANT MODULUS ALGORITHM BLIND EQUALIZATION AND PARTIAL DECODING	ENDRES, THOMAS J.
<a href="#">60129477</a>	Not Issued	159	04/15/1999	SYNCHRONIZATION ESTIMATION TECHNIQUE FOR A CONVOLUTIONAL DECODER	ENDRES, THOMAS J.
<a href="#">60129478</a>	Not Issued	159	04/15/1999	HYBRID REGRESSOR DECISION FEEDBACK EQUALIZER	ENDRES, THOMAS J.
<a href="#">60148983</a>	Not Issued	159	08/14/1999	CONSTANT MODULOUS DECISION FEEDBACK EQUALIZER	ENDRES, THOMAS J.
<a href="#">10332146</a>	Not Issued	30	06/11/2003	Method for identifying substances which are suitable to be used as medicaments for the treating virus infections or for testing the effectiveness of such substances	ENDRESS, THOMAS

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PALM INTRANET

**Inventor Name Search Result**

Your Search was:

Last Name = STROLLE

First Name = CHRISTOPHER

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">08919577</a>	Not Issued	161	02/20/1997	NUMERICAL VOLTAGE CONTROLLED OSCILLATOR	STROLLE, CHRISTOPHER
<a href="#">09457496</a>	<a href="#">6668014</a>	150	12/09/1999	EQUALIZER METHOD AND APPARATUS USING CONSTANT MODULUS ALGORITHM BLIND EQUALIZATION AND PARTIAL DECODING	STROLLE, CHRISTOPHER
<a href="#">10312931</a>	Not Issued	30	06/06/2003	Robust data extension for 8vsb signaling	STROLLE, CHRISTOPHER H
<a href="#">09549368</a>	Not Issued	93	04/14/2000	HYBRID SOFT AND HARD DECISION FEEDBACK EQUALIZER	STROLLE, CHRISTOPHER H.
<a href="#">09549369</a>	Not Issued	161	04/14/2000	Synchronization method and apparatus for a convolutional decoder	STROLLE, CHRISTOPHER H.
<a href="#">09638044</a>	Not Issued	161	08/12/2000	Constant modulus decision feedback equalizer	STROLLE, CHRISTOPHER H.
<a href="#">09716651</a>	<a href="#">6850563</a>	150	11/20/2000	DATA SLICER FOR COMBINED TRELLIS DECODING AND EQUALIZATION	STROLLE, CHRISTOPHER H.
<a href="#">09761303</a>	Not Issued	71	01/17/2001	Blind cost criterion timing recovery	STROLLE, CHRISTOPHER H.
<a href="#">09896844</a>	Not Issued	161	06/30/2001	Receiver for robust data extension for 8VSB signaling	STROLLE, CHRISTOPHER H.
<a href="#">09922006</a>	Not Issued	93	08/03/2001	CARRIER PHASE ESTIMATION BASED ON SINGLE-AXIS CONSTANT MODULUS COST CRITERION AND BUSSGANG CRITERIA	STROLLE, CHRISTOPHER H.
<a href="#">10016665</a>	Not Issued	71	12/11/2001	Linear prediction based initialization of a single-axis blind equalizer for VSB signals	STROLLE, CHRISTOPHER H.



<u>10213854</u>	Not Issued	30	08/07/2002	DC offset correction for constant modulus equalization	STROLLE, CHRISTOPHER H.
<u>10251650</u>	Not Issued	71	09/20/2002	Receiver for robust data extension for 8VSB signaling	STROLLE, CHRISTOPHER H.
<u>10384048</u>	Not Issued	95	03/07/2003	DIVERSITY RECEIVER WITH JOINT AUTOMATIC GAIN CONTROL SIGNAL PROCESSING	STROLLE, CHRISTOPHER H.
<u>10408881</u>	<u>6937677</u>	150	04/09/2003	DIVERSITY RECEIVER WITH JOINT PHASE LOCKED LOOP FILTER	STROLLE, CHRISTOPHER H.
<u>10409371</u>	<u>6870892</u>	150	04/09/2003	DIVERSITY RECEIVER WITH JOINT BAUD CLOCK RECOVERY	STROLLE, CHRISTOPHER H.
<u>60215703</u>	Not Issued	159	07/01/2000	Robust data extension for 8VSB signaling	STROLLE, CHRISTOPHER H.
<u>60242306</u>	Not Issued	159	10/20/2000	Constant modulus timing recovery	STROLLE, CHRISTOPHER H.
<u>60248941</u>	Not Issued	159	11/15/2000	Carrier phase detector based on single-axis constant modulus criterion	STROLLE, CHRISTOPHER H.
<u>60254829</u>	Not Issued	159	12/12/2000	Linear prediction based initialization of single-axis blind equalizers for vsb modulations	STROLLE, CHRISTOPHER H.
<u>60280944</u>	Not Issued	159	04/02/2001	Backward compatible extensions to A/53	STROLLE, CHRISTOPHER H.
<u>60313161</u>	Not Issued	159	08/17/2001	Behavior and corrections of constant modulus equalization with a DC offset	STROLLE, CHRISTOPHER H.
<u>06298092</u>	<u>4415928</u>	150	08/31/1981	CALCULATION OF RADIAL COORDINATES OF POLAR-COORDINATE RASTER SCAN	STROLLE, CHRISTOPHER H.
<u>06298268</u>	<u>4460958</u>	150	08/31/1981	WINDOW-SCANNED MEMORY	STROLLE, CHRISTOPHER H.
<u>06298269</u>	<u>4434437</u>	150	08/31/1981	GENERATING ANGULAR COORDINATE OF RASTER SCAN OF POLAR-COORDINATE ADDRESSED MEMORY	STROLLE, CHRISTOPHER H.
<u>06304924</u>	<u>4446529</u>	150	09/23/1981	LINEAR INTERPOLATION BETWEEN REGULARLY SPACED DIGITAL SAMPLES	STROLLE, CHRISTOPHER H.
<u>06305084</u>	<u>4471349</u>	150	09/24/1981	PHANTOM RASTER GENERATING APPARATUS SCANNING TV IMAGE	STROLLE, CHRISTOPHER H.

				MEMORY IN ANGULAR AND ORTHOGONAL COORDINATES	
<u>06305171</u>	<u>4432009</u>	150	09/24/1981	VIDEO PRE-FILTERING IN PHANTOM RASTER GENERATING APPARATUS	STROLLE, CHRISTOPHER H.
<u>06305173</u>	<u>4462024</u>	150	09/24/1981	MEMORY SCANNING ADDRESS GENERATOR	STROLLE, CHRISTOPHER H.
<u>06305174</u>	<u>4442545</u>	150	09/24/1981	COMPACTION OF TELEVISION DISPLAY GRAPHICS IN PHANTOM-RASTER-SCANNED IMAGE MEMORY	STROLLE, CHRISTOPHER H.
<u>06327631</u>	Not Issued	164	12/04/1981	TV GRAPHIC DISPLAYS WITHOUT QUANTIZING ERRORS FROM COMPACT IMAGE MEMORY	STROLLE, CHRISTOPHER H.
<u>06577507</u>	<u>4597007</u>	150	02/06/1984	CIRCUITRY FOR CORRECTING FRAME COMBED LUMINANCE SIGNAL FOR MOTION INDUCED DISTORTION	STROLLE, CHRISTOPHER H.
<u>06615093</u>	Not Issued	161	05/29/1984	SPATIAL-TEMPORAL FREQUENCY INTERLEAVING OF THE SPECTRUM OF A TELEVISION SIGNAL BY MEANS OF RECTANGULARLY SHAPED INTERLEAVED SECTIONS	STROLLE, CHRISTOPHER H.
<u>06695224</u>	<u>4656467</u>	150	01/25/1985	TV GRAPHIC DISPLAYS WITHOUT QUANTIZING ERRORS FROM COMPACT IMAGE MEMORY	STROLLE, CHRISTOPHER H.
<u>06741591</u>	<u>4683490</u>	150	06/05/1985	VIDEO SIGNAL PROCESSING APPARATUS	STROLLE, CHRISTOPHER H.
<u>06824902</u>	<u>4661850</u>	150	01/31/1986	PROGRESSIVE SCAN TELEVISION SYSTEM WITH INTERLACED INTER-FIELD SUM AND DIFFERENCE COMPONENTS	STROLLE, CHRISTOPHER H.
<u>07078150</u>	<u>4816899</u>	150	07/27/1987	COMPATIBLE WIDESCREEN TELEVISION SYSTEM	STROLLE, CHRISTOPHER H.
<u>07228193</u>	<u>4914507</u>	150	08/04/1988	VIDEO SIGNAL TIME EXPANSION/COMPRESSION APPARATUS USING PROGRAMMED OUTPUT VALUES	STROLLE, CHRISTOPHER H.
<u>07513758</u>	Not	161	04/24/1990	PLAYBACK EQUALIZER FOR A	STROLLE,

	Issued			TAPE RECORDER	CHRISTOPHER H.
<u>07531070</u>	Not Issued	161	05/31/1990	COLOR-UNDER CHROMA CHANNEL ENCODED WITH AUXILIARY SIGNALS	STROLLE, CHRISTOPHER H.
<u>07562907</u>	5430497	150	08/06/1990	REMOVAL OF THE FOLDING CARRIER AND SIDEBANDS FROM AN UNFOLDED VIDEO SIGNAL	STROLLE, CHRISTOPHER H.
<u>07569029</u>	5113262	150	08/17/1990	VIDEO SIGNAL RECORDING SYSTEM	STROLLE, CHRISTOPHER H.
<u>07604493</u>	Not Issued	161	10/26/1990	ADAPTIVE DEEMPHASIS AND REEMPHASIS OF HIGH FREQUENCIES IN A VIDEO SIGNAL	STROLLE, CHRISTOPHER H.
<u>07604494</u>	Not Issued	161	10/26/1990	ADAPTIVE DEEMPHASIS AND REEMPHASIS OF HIGH FREQUENCIES IN A VIDEO SIGNAL UTILIZING A RECORDED CONTROL SIGNAL	STROLLE, CHRISTOPHER H.
<u>07609536</u>	Not Issued	166	11/05/1990	APPARATUS AND METHOD FOR COLOR-UNDER CHROMA CHANNEL ENCODED WITH A HIGH FREQUENCY LUMINANCE SIGNAL	STROLLE, CHRISTOPHER H.
<u>07635197</u>	Not Issued	161	01/02/1991	VIDEO SIGNAL RECORDING SYSTEM	STROLLE, CHRISTOPHER H.
<u>07657029</u>	Not Issued	161	02/20/1991	SPATIAL FILTER FOR IMPROVED VHS SYSTEM	STROLLE, CHRISTOPHER H.
<u>07711981</u>	5321507	150	06/07/1991	MOTION SIGNAL DETECTING CIRCUIT	STROLLE, CHRISTOPHER H.
<u>07742764</u>	5267035	150	08/08/1991	MOTION DETECTION FOR VIDEO INCLUDING THAT OBTAINED FROM FILM	STROLLE, CHRISTOPHER H.
<u>07787690</u>	6134373	150	11/04/1991	SYSTEM FOR RECORDING AND REPRODUCING A WIDE BANDWIDTH VIDEO SIGNAL VIA A NARROW BANDWIDTH MEDIUM	STROLLE, CHRISTOPHER H.

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**Inventor Name Search Result**

Your Search was:

Last Name = HULYALKAR

First Name = SAMIR

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">08767101</a>	6069901	150	12/04/1996	ENERGY BURSTS FOR WIRELESS NETWORKS	HULYALKAR, SAMIR
<a href="#">09002559</a>	Not Issued	161	12/31/1997	WIRELESS UNIVERSAL BUILDING INFORMATION INFRASTRUCTURE	HULYALKAR, SAMIR
<a href="#">09093212</a>	6445690	150	06/08/1998	WIRELESS COUPLING OF INCOMPATIBLE NODES VIA A VIRTUAL NETWORK	HULYALKAR, SAMIR
<a href="#">09218559</a>	6529519	150	12/22/1998	PRIORITIZED-BUFFER MANAGEMENT FOR FIXED SIZED PACKETS IN MULTIMEDIA APPLICATION	HULYALKAR, SAMIR
<a href="#">09000672</a>	6032261	150	12/30/1997	BUS BRIDGE WITH DISTRIBUTION OF A COMMON CYCLE CLOCK TO ALL BRIDGE PORTALS TO PROVIDE SYNCHRONIZATION OF LOCAL BUSES, AND METHOD OF OPERATION THEREOF	HULYALKAR, SAMIR N.
<a href="#">08770024</a>	6198728	150	12/19/1996	MEDIUM ACCESS CONTROL (MAC) PROTOCOL FOR WIRELESS ATM	HULYALKAR, SAMIR N
<a href="#">09549368</a>	Not Issued	93	04/14/2000	HYBRID SOFT AND HARD DECISION FEEDBACK EQUALIZER	HULYALKAR, SAMIR N.
<a href="#">09549369</a>	Not Issued	161	04/14/2000	Synchronization method and apparatus for a convolutional decoder	HULYALKAR, SAMIR N.
<a href="#">09638044</a>	Not Issued	161	08/12/2000	Constant modulus decision feedback equalizer	HULYALKAR, SAMIR N.

<u>09716651</u>	<u>6850563</u>	150	11/20/2000	DATA SLICER FOR COMBINED TRELLIS DECODING AND EQUALIZATION	HULYALKAR, SAMIR N.
<u>09761303</u>	Not Issued	71	01/17/2001	Blind cost criterion timing recovery	HULYALKAR, SAMIR N.
<u>09864129</u>	<u>6697650</u>	150	05/24/2001	DATA TRANSMISSION SYSTEM FOR REDUCING TERMINAL POWER CONSUMPTION IN A WIRELESS NETWORK	HULYALKAR, SAMIR N.
<u>09896844</u>	Not Issued	161	06/30/2001	Receiver for robust data extension for 8VSB signaling	HULYALKAR, SAMIR N.
<u>09922006</u>	Not Issued	93	08/03/2001	CARRIER PHASE ESTIMATION BASED ON SINGLE-AXIS CONSTANT MODULUS COST CRITERION AND BUSSGANG CRITERIA	HULYALKAR, SAMIR N.
<u>10016665</u>	Not Issued	71	12/11/2001	Linear prediction based initialization of a single-axis blind equalizer for VSB signals	HULYALKAR, SAMIR N.
<u>10080184</u>	Not Issued	61	02/20/2002	Apparatus and method for peer-to-peer link monitoring of a wireless network with centralized control	HULYALKAR, SAMIR N.
<u>10251650</u>	Not Issued	71	09/20/2002	Receiver for robust data extension for 8VSB signaling	HULYALKAR, SAMIR N.
<u>10253245</u>	Not Issued	71	09/24/2002	Dual loop automatic gain control	HULYALKAR, SAMIR N.
<u>10384048</u>	Not Issued	95	03/07/2003	DIVERSITY RECEIVER WITH JOINT AUTOMATIC GAIN CONTROL SIGNAL PROCESSING	HULYALKAR, SAMIR N.
<u>10409371</u>	<u>6870892</u>	150	04/09/2003	DIVERSITY RECEIVER WITH JOINT BAUD CLOCK RECOVERY	HULYALKAR, SAMIR N.
<u>10663028</u>	Not Issued	30	09/15/2003	Method and apparatus for controlling a smart antenna using metrics derived from a single carrier digital signal	HULYALKAR, SAMIR N.
<u>10973486</u>	Not Issued	30	10/26/2004	Trellis decoder for decoding data stream including symbols coded with multiple convolutional codes	HULYALKAR, SAMIR N.
<u>60242306</u>	Not Issued	159	10/20/2000	Constant modulus timing recovery	HULYALKAR, SAMIR N.

<u>60248941</u>	Not Issued	159	11/15/2000	Carrier phase detector based on single-axis constant modulus criterion	HULYALKAR, SAMIR N.
<u>60254829</u>	Not Issued	159	12/12/2000	Linear prediction based initialization of single-axis blind equalizers for vsb modulations	HULYALKAR, SAMIR N.
<u>60475736</u>	Not Issued	159	06/04/2003	Method and apparatus for controlling a smart antenna using metrics derived from a single carrier digital signal	HULYALKAR, SAMIR N.
<u>07854177</u>	<u>5291289</u>	150	03/20/1992	METHOD AND APPARATUS FOR TRANSMISSION AND RECEPTION OF A DIGITAL TELEVISION SIGNAL USING MULTICARRIER MODULATION	HULYALKAR, SAMIR N.
<u>08060181</u>	Not Issued	166	05/07/1993	METHOD AND APPARATUS FOR MULTIPLEXED QUADRATURE AMPLITUDE MODULATION (QAM) TELEVISION TRANSMISSION	HULYALKAR, SAMIR N.
<u>08114545</u>	Not Issued	161	08/31/1993	METHOD AND APPARATUS FOR TRANSMISSION OF PROGRESSIVELY SCANNED HIGH DEFINITION TELEVISION PICTURES	HULYALKAR, SAMIR N.
<u>08170471</u>	Not Issued	166	12/20/1993	METHOD AND APPARATUS FOR REDUCING PRECODING LOSS WHEN USING A POST-COMB FILTERING APPROACH TO REDUCE CO-CHANNEL INTERFERENCE IN HIGH DEFINITION TELEVISION TRANSMISSION	HULYALKAR, SAMIR N.
<u>08181643</u>	Not Issued	161	01/13/1994	METHOD AND APPARATUS FOR TRANSMISSION AND RECEPTION OF A DIGITAL TELEVISION SIGNAL USING MULTICARRIER MODULATION	HULYALKAR, SAMIR N.
<u>08197773</u>	<u>5452015</u>	150	02/10/1994	METHOD AND APPARATUS FOR COMBATING CO-CHANNEL NTSC INTERFERENCE FOR DIGITAL TV TRANSMISSION	HULYALKAR, SAMIR N.

<u>08230360</u>	<u>5512957</u>	150	04/20/1994	METHOD AND APPARATUS FOR COMBATING CO-CHANNEL NTSC INTERFERENCE FOR DIGITAL TV TRANSMISSION	HULYALKAR, SAMIR N.
<u>08327175</u>	<u>5832041</u>	250	10/21/1994	64 QAM SIGNAL CONSTELLATION WHICH IS ROBUST IN THE PRESENCE OF PHASE NOISE AND HAS LOW DECODING COMPLEXITY	HULYALKAR, SAMIR N.
<u>08366136</u>	<u>5565926</u>	150	12/29/1994	COMMUNICATING DIGITAL TELEVISION SIGNALS USING A SIGNAL CONSTELLATION FORMED BY FOUR SIGNAL CONSTELLATIONS PLACED IN THE QUADRANTS	HULYALKAR, SAMIR N.
<u>08366340</u>	<u>5606369</u>	150	12/28/1994	BUFFERING FOR DIGITAL VIDEO SIGNAL ENCODERS USING JOINT BIT-RATE CONTROL	HULYALKAR, SAMIR N.
<u>08396289</u>	<u>5561468</u>	150	02/28/1995	MULTIPLEXED MODULATION SYSTEM FOR ROBUST AUDIO DIGITAL TELEVISION	HULYALKAR, SAMIR N.
<u>08420891</u>	<u>5619534</u>	150	04/11/1995	METHOD AND APPARATUS FOR REDUCING PRECODING LOSS WHEN USING A POST-COMB FILTERING APPROACH TO REDUCE CO-CHANNEL INTERFERENCE IN HIGH DEFINITION TELEVISION TRANSMISSION	HULYALKAR, SAMIR N.
<u>08444484</u>	<u>5648822</u>	150	05/19/1995	METHOD AND APPARATUS FOR COMBATING CO-CHANNEL NTSC INTERFERENCE USING A VARIABLE-COMB FILTER FOR DIGITAL TV TRANSMISSION	HULYALKAR, SAMIR N.
<u>08550128</u>	<u>5602602</u>	150	10/27/1995	METHOD AND APPARATUS FOR COMBATING CO-CHANNEL NTSC INTERFERENCE FOR	HULYALKAR, SAMIR N.

				DIGITAL TV TRANSMISSION HAVING A SIMPLIFIED REJECTION FILTER	
<u>08573857</u>	<u>5761088</u>	150	12/18/1995	METHOD AND APPARATUS FOR CHANNEL IDENTIFICATION USING INCOMPLETE OR NOISY INFORMATION	HULYALKAR, SAMIR N.
<u>08576546</u>	Not Issued	168	12/21/1995	BLIND EQUALIZER METHOD AND APPARATUS FOR HDTV TRANSMISSION USING AN NTSC REJECTION FILTER FOR MITIGATING CO- CHANNEL INTERFERENCE	HULYALKAR, SAMIR N.
<u>08577685</u>	<u>5745187</u>	150	12/21/1995	METHOD AND APPARATUS FOR COMBATING CO- CHANNEL NTSC INTERFERENCE FOR DIGITAL TV TRANSMISSION USING A BANK OF REJECTION FILTERS	HULYALKAR, SAMIR N.
<u>08641152</u>	<u>5881363</u>	150	04/29/1996	METHOD AND APPARATUS FOR COMBATting INGRESS AND MULTIPATH IN A CATV RETURN CHANNEL	HULYALKAR, SAMIR N.
<u>08656803</u>	<u>5787080</u>	150	06/03/1996	METHOD AND APPARATUS FOR RESERVATION-BASED WIRELESS-ATM LOCAL AREA NETWORK	HULYALKAR, SAMIR N.
<u>08715949</u>	<u>5878086</u>	150	09/19/1996	METHOD AND APPARATUS FOR PRODUCING A DETERMINISTIC SEQUENCE FROM AN IIR FILTER	HULYALKAR, SAMIR N.
<u>08887406</u>	<u>5841484</u>	250	07/02/1997	BLIND EQUALIZER METHOD AND APPARATUS FOR HDTV TRANSMISSION USING AN NTSC REJECTION FILTER FOR MITIGATING CO- CHANNEL INTERFERENCE	HULYALKAR, SAMIR N.
<u>08902049</u>	<u>6061826</u>	150	07/29/1997	A HARDWARE-OPTIMIZED REED-SOLOMON DECODER FOR LARGE DATA BLOCKS	HULYALKAR, SAMIR N.
<u>08919052</u>	<u>6751196</u>	150	08/27/1997	APPARATUS AND METHOD FOR PEER-TO-PEER LINK MONITORING OF A WIRELESS NETWORK WITH CENTRALIZED CONTROL	HULYALKAR, SAMIR N.



<u>09073095</u>	<u>6339585</u>	150	05/05/1998	ERROR-RECOVERY MECHANISM USING A TEMPORARY FORWARDER IN A WIRELESS-ATM NETWORK	HULYALKAR, SAMIR N.
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Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09457496</u>	6668014	150	12/09/1999	EQUALIZER METHOD AND APPARATUS USING CONSTANT MODULUS ALGORITHM BLIND EQUALIZATION AND PARTIAL DECODING	CASAS, RAUL A
<u>09761303</u>	Not Issued	71	01/17/2001	Blind cost criterion timing recovery	CASAS, RAUL A.
<u>09922006</u>	Not Issued	93	08/03/2001	CARRIER PHASE ESTIMATION BASED ON SINGLE-AXIS CONSTANT MODULUS COST CRITERION AND BUSSGANG CRITERIA	CASAS, RAUL A.
<u>10016665</u>	Not Issued	71	12/11/2001	Linear prediction based initialization of a single-axis blind equalizer for VSB signals	CASAS, RAUL A.
<u>10213854</u>	Not Issued	30	08/07/2002	DC offset correction for constant modulus equalization	CASAS, RAUL A.
<u>10663028</u>	Not Issued	30	09/15/2003	Method and apparatus for controlling a smart antenna using metrics derived from a single carrier digital signal	CASAS, RAUL A.
<u>10757536</u>	Not Issued	30	01/15/2004	Digital receiver having adaptive carrier recovery circuit	CASAS, RAUL A.
<u>10812279</u>	Not Issued	20	03/29/2004	Audio/video separator	CASAS, RAUL A.
<u>11090478</u>	Not Issued	30	03/25/2005	Low latency digital filter and method	CASAS, RAUL A.
<u>60242306</u>	Not Issued	159	10/20/2000	Constant modulus timing recovery	CASAS, RAUL A.
<u>60248941</u>	Not Issued	159	11/15/2000	Carrier phase detector based on single-axis constant modulus criterion	CASAS, RAUL A.

<a href="#">60254829</a>	Not Issued	159	12/12/2000	Linear prediction based initialization of single-axis blind equalizers for vsb modulations	CASAS, RAUL A.
<a href="#">60313161</a>	Not Issued	159	08/17/2001	Behavior and corrections of constant modulus equalization with a DC offset	CASAS, RAUL A.
<a href="#">60475736</a>	Not Issued	159	06/04/2003	Method and apparatus for controlling a smart antenna using metrics derived from a single carrier digital signal	CASAS, RAUL A.
<a href="#">05940743</a>	<a href="#">4240702</a>	150	09/08/1978	FIELD CORRECTOR FOR TWO MIRROR OBJECTIVE SYSTEMS	CASAS, RAUL E.
<a href="#">06162213</a>	<a href="#">4422046</a>	150	06/23/1980	AXISYMMETRIC SPATIAL/TEMPORAL ENCODER	CASAS, RAUL E.

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